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# KA5x02xx-SERIES

KA5H0265RC, KA5M0265R, KA5L0265R, KA5H02659RN/KA5M02659RN, KA5H0280R, KA5M0280R

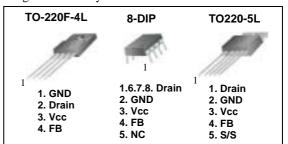
Fairchild Power Switch(FPS)

#### **Features**

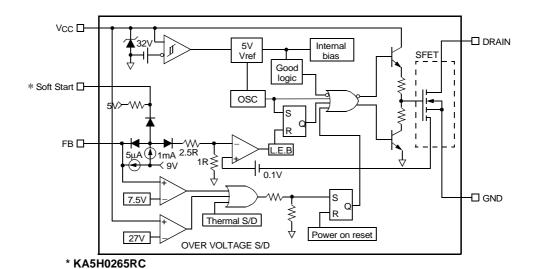
- Precision Fixed Operating Frequency (100/67/50kHz)
- Low Start-up Current (Typ. 100uA)
- Pulse by Pulse Current Limiting
- Over Load Protection
- Over Voltage Protection (Min. 25V)
- Internal Thermal Shutdown Function
- Under Voltage Lockout
- Internal High Voltage Sense FET
- · Auto-Restart Mode

### **Description**

The Fairchild Power Switch(FPS) product family is specially designed for an off-line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry-compared to discrete MOSFET and controller or RCC switching converter solution. The Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.



### **Internal Block Diagram**



## **Absolute Maximum Ratings**

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
KA5x0265xRx			
Drain-Gate Voltage (R <sub>GS</sub> =1MΩ)	VDGR	650	V
Gate-Source (GND) Voltage	Vgs	±30	V
Drain Current Pulsed (1)	IDM	8.0	ADC
Continuous Drain Current (T <sub>C</sub> =25°C)	ID	2.0	ADC
Continuous Drain Current (Tc=100°C)	ID	1.3	ADC
Single Pulsed Avalanche Energy (2)	Eas	68	mJ
Maximum Supply Voltage	VCC,MAX	30	V
Analog Input Voltage Range	VFB	-0.3 to V <sub>SD</sub>	V
Total Dawar Dissipation	PD	42	W
Total Power Dissipation —	Darting	0.33	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C
KA5x0280R			
Drain-Gate Voltage (R <sub>GS</sub> =1MΩ)	VDGR	800	V
Gate-Source (GND) Voltage	Vgs	±30	V
Drain Current Pulsed (1)	I <sub>DM</sub>	8.0	ADC
Continuous Drain Current (T <sub>C</sub> =25°C)	ID	2.0	ADC
Continuous Drain Current (T <sub>C</sub> =100°C)	ID	1.3	ADC
Single Pulsed Avalanche Energy (2)	Eas	90	mJ
Maximum Supply Voltage	VCC,MAX	30	V
Analog Input Voltage Range	VFB	-0.3 to V <sub>SD</sub>	V
Total Power Dissipation	PD	35	W
Total Fower Dissipation	Darting	0.28	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C

#### Note:

<sup>1.</sup> Repetitive rating: Pulse width limited by maximum junction temperature

<sup>2.</sup> L = 51mH, starting  $T_j$  = 25°C

## **Electrical Characteristics (SFET Part)**

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
KA5x0265xRx						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
	IDSS	V <sub>DS</sub> =Max. Rating, V <sub>GS</sub> =0V	-	-	50	μΑ
Zero Gate Voltage Drain Current		V <sub>DS</sub> =0.8Max. Rating, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C	-	-	200	μΑ
Static Drain-Source on Resistance (Note)	RDS(ON)	VGS=10V, ID=0.5A	-	5.0	6.0	Ω
Forward Transconductance (Note)	gfs	V <sub>DS</sub> =50V, I <sub>D</sub> =0.5A	1.5	2.5	-	S
Input Capacitance	Ciss	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-	550	-	pF
Output Capacitance	Coss	VGS=0V, VDS=25V, f=1MHz	-	38	-	
Reverse Transfer Capacitance	Crss	- 1-11/11/12	-	17	-	
Turn on Delay Time	td(on)	VDD=0.5B VDSS, ID=1.0A	-	20	-	nS
Rise Time	tr	(MOSFET switching time is	-	15	-	
Turn Off Delay Time	td(off)	essentially independent of	-	55	-	
Fall Time	tf	operating temperature)	-	25	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	V <sub>G</sub> S=10V, I <sub>D</sub> =1.0A, V <sub>D</sub> S=0.5B V <sub>D</sub> SS (MOSFET	-	-	35	nC
Gate-Source Charge	Qgs	switching time is essentially	-	3	-	
Gate-Drain (Miller) Charge	Qgd	<ul> <li>independent of operating temperature)</li> </ul>	-	12	-	
KA5x0280R	<u> </u>				I	
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	800	-	-	V
	I <sub>DSS</sub>	VDS=Max. Rating, VGS=0V	-	-	50	μΑ
Zero Gate Voltage Drain Current		V <sub>DS</sub> =0.8Max. Rating, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C	-	-	200	μА
Static Drain-Source on Resistance (Note)	RDS(ON)	VGS=10V, ID=0.5A	-	5.6	7.0	Ω
Forward Transconductance (Note)	gfs	VDS=50V, ID=0.5A	1.5	2.5	-	S
Input Capacitance	Ciss		-	250	-	pF
Output Capacitance	Coss	VGS=0V, VDS=25V, f=1MHz	-	52	-	
Reverse Transfer Capacitance	Crss	1-11/11/12	-	25	-	
Turn on Delay Time	td(on)	VDD=0.5B VDSS, ID=1.0A	-	21	-	nS
Rise Time	tr	(MOSFET switching time is	-	28	-	
Turn Off Delay Time	td(off)	essentially independent of	-	77	-	
Fall Time	tf	operating temperature)	-	24	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=1.0A, VDS=0.5B VDSS (MOSFET	-	-	60	nC
Gate-Source Charge	Qgs	switching time is essentially	-	15	-	
Gate-Drain (Miller) Charge	Qgd	independent of operating temperature)	-	20	-	

### Note:

$$^{2.} S = \frac{1}{R}$$

<sup>1.</sup> Pulse test: Pulse width  $\leq 300 \mu S$ , duty cycle  $\leq 2\%$ 

## Electrical Characteristics (Control Part) (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
UVLO SECTION				1	I	l
Start Threshold Voltage	VSTART	V <sub>FB</sub> =GND	14	15	16	V
Stop Threshold Voltage	VSTOP	V <sub>FB</sub> =GND	8.2	8.8	9.4	V
OSCILLATOR SECTION					I	
Initial Accuracy	Fosc	KA5H0265xRx KA5H0280R	90	100	110	kHz
Initial Accuracy	Fosc	KA5M0265xRx KA5M0280R	61	67	73	kHz
Initial Accuracy	Fosc	KA5L0265R	45	50	55	kHz
Frequency Change With Temperature (2)	ΔΕ/ΔΤ	-25°C ≤ Ta ≤ +85°C	-	±5	±10	%
Maximum Duty Cycle	Dmax	KA5H0265xRx KA5H0280R	62	67	72	%
Maximum Duty Cycle	Dmax	KA5M0265xRx KA5M0280R KA5L0265R	72	77	82	%
FEEDBACK SECTION					I	
Feedback Source Current	IFB	Ta=25°C, 0V ≤ Vfb ≤ 3V	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	VsD	Vfb ≤ 6.5V	6.9	7.5	8.1	V
Shutdown Delay Current	Idelay	Ta=25°C, 5V ≤ Vfb ≤ V <sub>SD</sub>	4	5	6	μΑ
SOFT START SECTION					•	
Soft Start Voltage	Vss	KA5H0265RC	4.7	5.0	5.3	V
Soft Start Current	Iss	RASHUZOSKC	8.0	1.0	1.2	mA
REFERENCE SECTION					•	
Output Voltage (1)	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability (1)(2)	Vref/∆T	-25°C ≤ Ta ≤ +85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT(SELF-PROTECTION)S	SECTION				•	
Peak Current Limit	lover	KA5x02659RN	0.79	0.9	1.01	А
Peak Current Limit	IOVER	KA5x0265Rx KA5x0280R	1.05	1.2	1.34	А
PROTECTION SECTION				u	I.	•
Over Voltage Protection	Vovp	Vcc ≥ 24V	25	27	29	V
Thermal Shutdown Temperature (1)	T <sub>SD</sub>	-	140	160	-	°C
TOTAL DEVICE SECTION						•
Start-up Current	ISTART	VCC=14V	-	100	170	μΑ
Operating Supply Current (Control Part Only)	IOPR	V <sub>CC</sub> ≤ 28	-	7	12	mA

#### Note:

- 1. These parameters, although guaranteed, are not 100% tested in production  $% \left( 1\right) =\left( 1\right) \left( 1\right$
- 2. These parameters, although guaranteed, are tested in EDS (wafer test) process

### **Typical Performance Characteristics**

(These characteristic graphs are normalized at Ta=25°C)

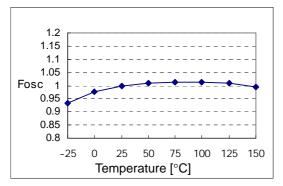


Figure 1. Operating Frequency

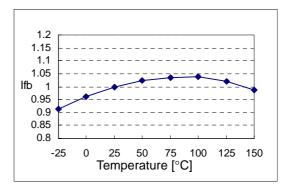


Figure 2. Feedback Source Current

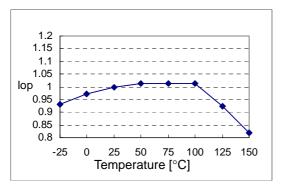


Figure 3. Operating Supply Current

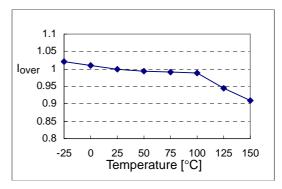


Figure 4. Peak Current Limit

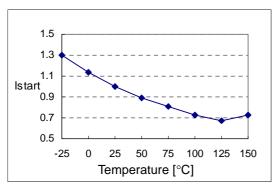


Figure 5. Start up Current

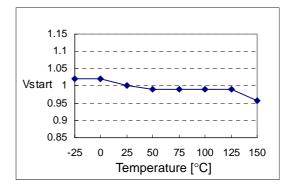


Figure 6. Start Threshold Voltage

### **Typical Performance Characteristics (Continued)**

(These characteristic graphs are normalized at Ta=25°C)

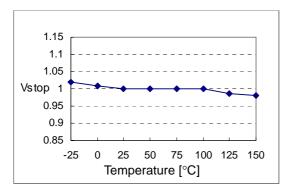


Figure 7. Stop Threshold Voltage

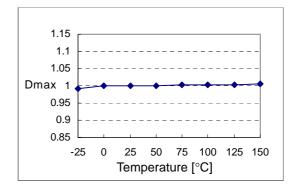


Figure 8. Maximum Duty Cycle

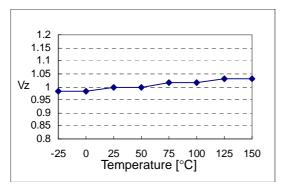


Figure 9. VCC Zener Voltage

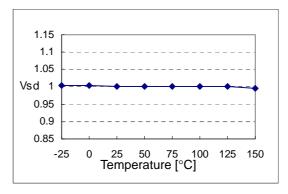


Figure 10. Shutdown Feedback Voltage

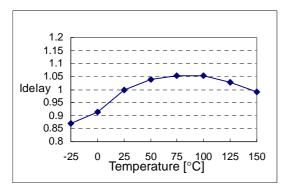


Figure 11. Shutdown Delay Current

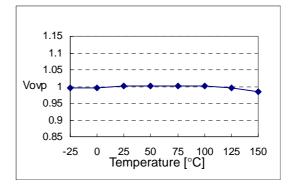


Figure 12. Over Voltage Protection

## **Typical Performance Characteristics** (Continued)

(These characteristic graphs are normalized at Ta=25°C)

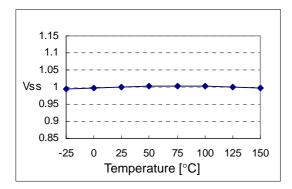


Figure 13. Soft Start Voltage

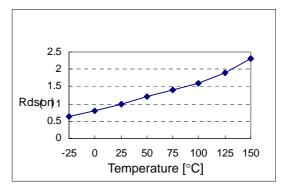
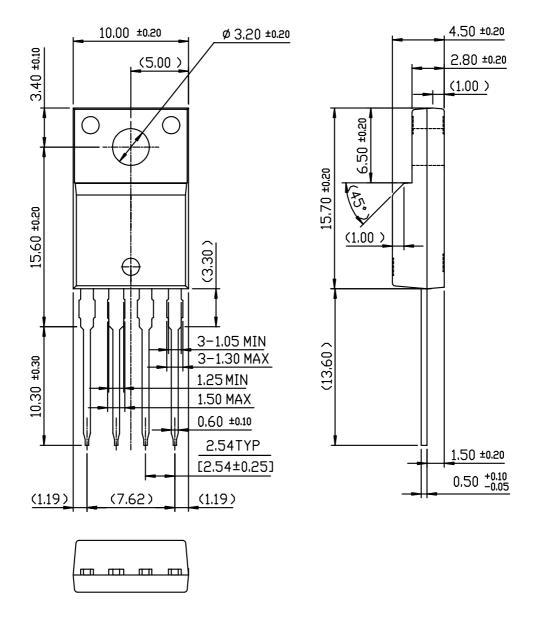


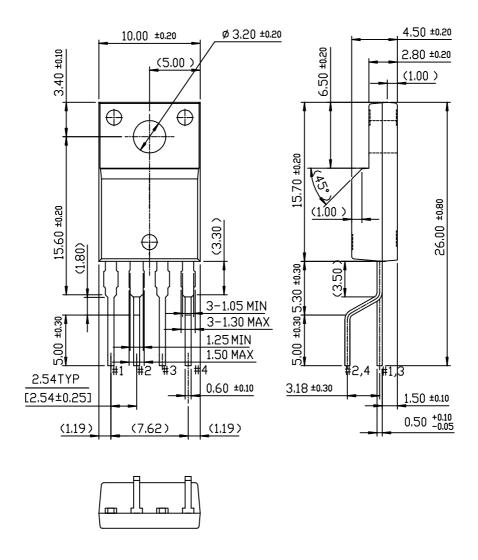
Figure 14. Static Drain-Source on Resistance

### **Package Dimensions**

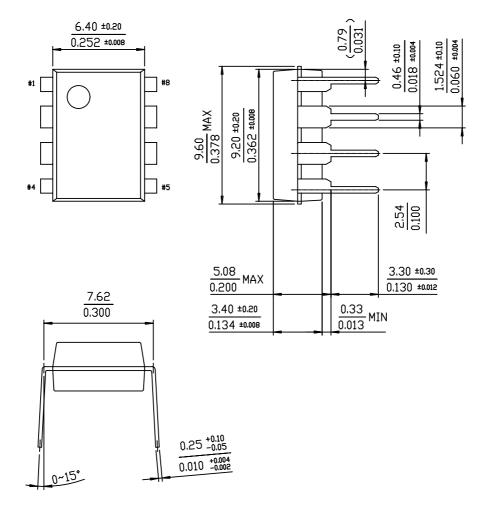
TO-220F-4L



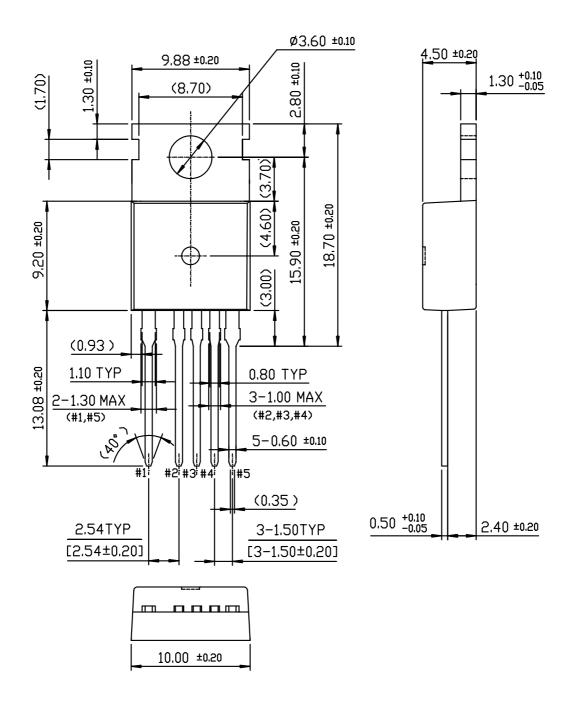
# TO-220F-4L(Forming)



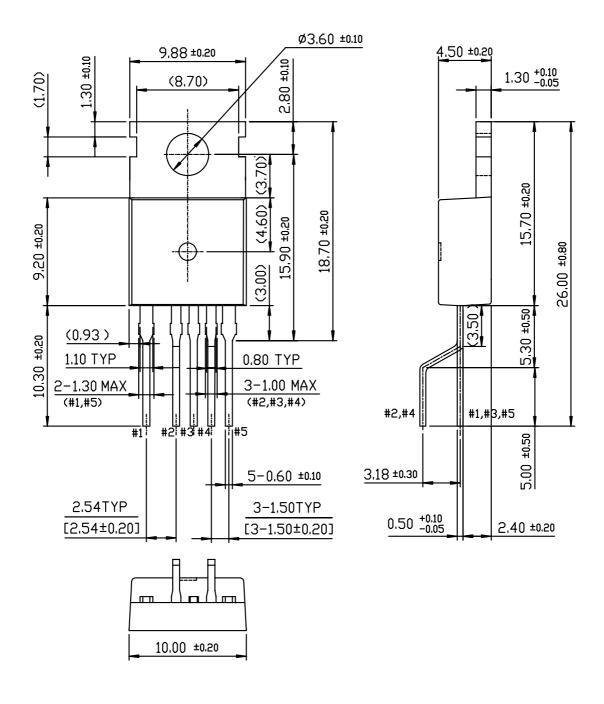
## 8-DIP



## TO-220-5L



# TO-220-5L(Forming)



# **Ordering Information**

Product Number	Package	Marking Code	BVDSS	Fosc	RDS(on)	
KA5H0265RCTU	TO-220-5L	5H0265RC	0265RC 650V	100kHz	5Ω	
KA5H0265RCYDTU	TO-220-5L(Forming)	3110203110	030 V	TOOKITZ	322	
KA5M0265RTU	TO-220F-4L	5M0265R	650V	67kHz	5Ω	
KA5M0265RYDTU	TO-220F-4L(Forming)	31V1U203K	650 V	67KHZ	522	
KA5L0265RTU	TO-220F-4L	EL 026ED	650V	FOLUE	5Ω	
KA5L0265RYDTU	TO-220F-4L(Forming)	5L0265R	650 V	50kHz	522	
Product Number	Package	Marking Code	BVDSS	Fosc	RDS(on)	
KA5H0280RTU	TO-220F-4L	5H0280R	800V	100kHz	5.6Ω	
KA5H0280RYDTU	TO-220F-4L(Forming)	3H0260K	800 V	TOOKHZ	5.622	
KA5M0280RTU	TO-220F-4L	5M0280R	800V	67kHz	F.60	
KA5M0280RYDTU	TO-220F-4L(Forming)	SIVIUZOUR	800 V	07KHZ	5.6Ω	
Product Number	Package	Marking Code	BVDSS	Fosc	RDS(on)	
KA5H02659RN	8-DIP	5H02659R	650V	100kHz	5Ω	
KA5M02659RN	8-DIP	5M02659R	650V	67kHz	5Ω	

TU: Non Forming Type YDTU: Forming Type

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